

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

In Re PATENT APPLICATION OF:

Applicants : Noriyuki MIURA)
Serial No. : To Be Assigned)
Filed : October 23, 2003)
For : SEMICONDUCTOR DEVICE AND)
METAL-OXIDE-SEMICONDUCTOR)
FIELD-EFFECT TRANSISTOR)
Attorney Ref. : MAE 296)

**INFORMATION
DISCLOSURE
STATEMENT**

October 23, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

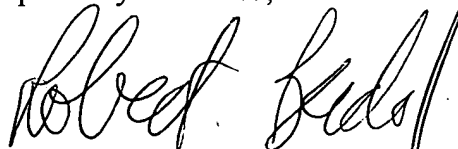
Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of one Japanese publication and an English-language article. Any relevance of the Japanese publication can be gleaned from the attached English-language abstract, and from page 1 of the present application, where these documents are discussed. The documents are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted documents is respectfully requested.

Respectfully submitted,



Robert H. Berdo, Jr. (Reg. No. 38,075)
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(202) 371-8976
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October 23, 2003
Date

RHB:tz

FEE ENCLOSED: \$810
Please charge any further
fee to our Deposit Account
No. 18-0002

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket		Application No.	
				MAE 296		To Be Assigned	
				Applicant			
				Noriyuki MIURA			
				Filing Date		Group Unit	
				October 23, 2003		To Be Assigned	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub-Class	Translation
	AH	2001-284590	10/12/01	Japan			Abstract
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
	AO	Su et al., "Optimization of Series Resistance in Sub-0.2 μ m SOI MOSFETs", 1993, IEDM 93, p723-726.					
	AP						
	AQ						
	AR						
Examiner					Date Considered		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							